

# APPROVAL SHEET

# **WW25M**

±1%, ±5%

Metal Plate Low Ohm Power Chip Resistors

Size 2512 (6432) 1W

**Current Sensing Type** 

\*Contents in this sheet are subject to change without prior notice.



#### **FEATURE**

- 1. Ultra low and stable TCR performance
- 2. High power rating and compact size
- 3. High reliability and stability
- 4. Reduced size of final equipment
- 5. RoHS compliant and Lead free products
- 6. Inductance below 1.0 nH

#### **APPLICATION**

- Power supply
- PDA
- Digital meter
- Computer
- Automotives
- Battery charger
- DC-DC power converter

#### **DESCRIPTION**

The resistors are constructed in a high grade low resistive metal body. The resistive layer is covered with a protective coat and printed a resistance marking code over it. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is Tin (lead-free) soder alloy.

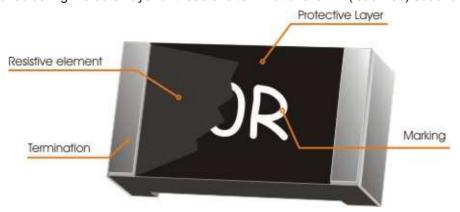


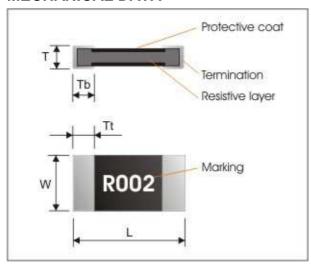
Fig 1. Construction of Chip-R



# **QUICK REFERENCE DATA**

Item	General Specification		
Series No.	WW25M		
Size code	2512 ( 6432 )		
Resistance Tolerance	±5%; ±1%		
Resistance Range	$0.003\Omega,0.004\Omega,$	$\begin{array}{c} 0.005\Omega,0.006\Omega,0.007\Omega,0.008\Omega,0.009\Omega,\\ 0.010\Omega,0.012\Omega,0.015\Omega,0.020\Omega,0.025\Omega,\\ 0.030\Omega,0.033\Omega,0.035\Omega,0.050\Omega \end{array}$	
TCR (ppm/°C)	≤ ±100 ppm/°C ≤ ±70 ppm/°C		
Max. dissipation at T <sub>amb</sub> =70°C	1 W		
Operation temperature	-55 ~ +155'C		

# **MECHANICAL DATA**



Symbol	Dimensions (mm)		
L	6.20±0.20		
W	3.20±0.20		
Т	0.60±0.20		
Tt	0.80±0.20		
Tb	0.80±0.20		



#### **MARKING**

Each resistor is marked with a four-digit code on the protective coating to designate the nominal resistance value.

Example:

$$\label{eq:R005} \begin{split} R005 &= 0.005\Omega \\ R010 &= 0.010\Omega \end{split}$$

#### **FUNCTIONAL DESCRIPTION**

#### **Derating curve**

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

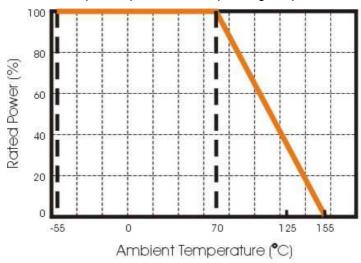


Fig.2 Maximum dissipation in percentage of rated power As a function of the ambient temperature

#### **MOUNTING**

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.



#### **SOLDERING CONDITION**

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

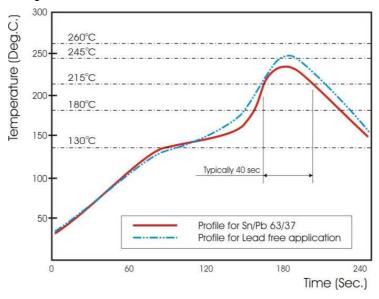


Fig 3. Infrared soldering profile for Chip Resistors WW25

#### **CATALOGUE NUMBERS**

The resistors have a catalogue number starting with .

WW25	М	R005	J	Т	L
Size code	Type code	Resistance code	Tolerance	Packaging code	Termination code
WW25 : 2512	M : 1W Sensing type	R is first digit followed by 3 significant digits. $0.010\Omega = R010$ $0.005\Omega = R005$ $0.050\Omega = R050$	J : ±5% F : ±1%	T: 7" reeled in tape Q: 10" reeled in tape	L = Sn base (lead free)

Reeled tape packaging : 12mm width plastic taping 4,000pcs per 7" reel; 8,000pcs per 10" reel.



# **TEST AND REQUIREMENTS(JIS C 5201-1: 1998)**

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category LCT/UCT/56(rated temperature range: Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, subclause 5.3. Unless otherwise specified, the following value supplied:

Temperature: 15°C to 35°C. Relative humidity: 45% to 75%.

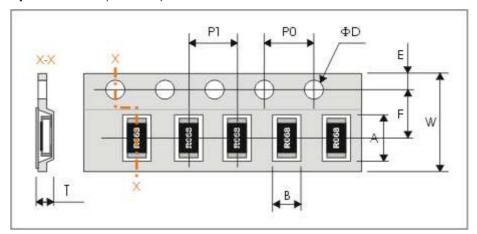
Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar). All soldering tests are performed with midly activated flux.

TEST	PROCEDURE	REQUIREMENT
Temperature Coefficient of Resistance(T.C.R) Clause 4.8	Natural resistance change per change in degree centigrade. $\frac{R_2-R_1}{R_1\big(t_2-t_1\big)}\times 10^6 \ \text{(ppm/°C)}  t_1:20\text{°C}+5\text{°C}-1\text{°C}$ $R_1: \text{Resistance at reference temperature}$ $R_2: \text{Resistance at test temperature}$	Refer to "QUICK REFERENCE DATA"
Short time overload (S.T.O.L) Clause 4.13	Permanent resistance change after a 5second application of a voltage 2.5 times RCWV or the maximum overload voltage specified in the above list, whichever is less.	no visible damage $ \Delta R/R \text{ max.} \\ \pm (1\% + 0.0001\Omega) $
Resistance to soldering heat(R.S.H)  Clause 4.18	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 260°C±5°C	no visible damage $ \Delta R/R \ max. \\ \pm (1\% + 0.0001\Omega) $
Solderability Clause 4.17	Un-mounted chips completely immersed for 2±0.5second in a SAC solder bath at 235 $^\circ\!$	good tinning (>95% covered) no visible damage
Temperature cycling Clause 4.19	30 minutes at -55°C±3°C, 2~3 minutes at 20°C+5°C-1°C, 30 minutes at +155°C±3°C, 2~3 minutes at 20°C+5°C-1°C, total 5 continuous cycles	no visible damage $ \Delta R/R \text{ max.} \\ \pm (1\% + 0.0005\Omega) $
Load life (endurance) Clause 4.25	1000 +48/-0 hours, loaded with RCWV or Vmax in chamber controller 70±2°C, 1.5 hours on and 0.5 hours off	no visible damage $ \Delta R/R \text{ max.} \\ \pm (1\% + 0.0005\Omega) $
Load life in Humidity Clause 4.24	1000 +48/-0 hours, loaded with RCWV or Vmax in humidity chamber controller at 40°C±2°C and 90~95% relative humidity, 1.5hours on and 0.5 hours off	no visible damage $ \Delta R/R \ max. \\ \pm (1\% + 0.0005\Omega) $
Adhesion Clause 4.32	Pressurizing force: 5N, Test time: 10±1sec.	No remarkable damage or removal of the terminations



#### **PACKAGING**

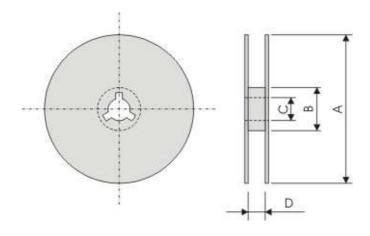
# Plastic Tape specifications (unit :mm)



Symbol	Α	В	W	F	E
Dimensions	6.75±0.20	3.50±0.20	12.00±0.30	5.50±0.1	1.75±0.10

Symbol	P1	P0	ΦD	Т
Dimensions	4.00±0.10	4.00±0.10	Ф1.50 <sup>+0.1</sup> <sub>-0.0</sub>	Max. 1.2

#### **Reel dimensions**



Symbol	Α	В	С	D
7" reel	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	12.4±1.0
10" reel	Φ254.0±2.0	Φ100.0±1.0	13.0±0.2	14.0±0.2

# **Taping quantity**

- Chip resistors 4,000 pcs per 7" reel. Chip resistors 8,000 pcs per 10" reel.